

Manuel Carro Liñares Profesor Titular

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Manuel Carro received his Bachelor degree in Computer Science from the Technical University of Madrid (UPM), and his Ph.D. degree from the same University in 2003. He is currently an Associate Professor at the Technical University of Madrid, Associate Research Professor at the IMDEA Software Institute and, since May 2017, its Scientific Director. He is the representative of the Institute at Informatics Europe and at the Node Strategy Committee of EIT Digital Spain and a member of the Technical Transfer Committee of the Alastria, and Spanish association aiming at

He has previously been Deputy Director at the IMDEA Software Institute, representative of UPM at the NESSI and INES technological platforms, prepresentative of UPM at SpaRCIM, deputy representative of IMDEA Software at ERCIM, and CLC Manager and Scientific Coordinator of the Madrid Node of EIT Digital.

He has published over 90 papers in international conferences and journals, and received best paper awards at ICLP 2005 and ICSOC 2011. He has been organizer and PC member of many international conferences and workshops, including conference chair of ICLP 2014 and PC Chair of ICLP 2016, the flagship conference in the field of Logic Programming. He has been PI in research projects at the regional, national, and European level, including S-Cube European Network of Excellence. He is currently PI of a national and a regional research project.

He has completed the supervision of five Ph.D. theses

spreading the usage of blockchain technolologies.

His interests span several topics, including the design and implementation of high-level (logicand constraint-based) programming languages to express non-monotonic knowledge and reasoning and to improve the quality of software production, the analysis of service-based systems, and the effective usage of formal specifications in teaching programming. He has long been interested in parallel programming, parallel implementations of declarative languages, and visualization of program execution.