



---

Oscar Garnica has a graduate degree in Physics (B.S. in Physics and M.S. in Electrical Engineering), and a Ph.D. in Physics (Program in Computer Science). He has about 25 years of experience in the fields of circuit and processor design. Currently, Oscar is an Associate Professor in the Department of Computer Architecture and System Engineering at Universidad Complutense de Madrid (UCM). He belongs to the Group of Adaptive and Bioinspired Systems (ABSys). Previously he has held several positions as Assistant Professor in the UCM, and ASIC Design Engineer in Lucent Technologies Bell Labs Innovations, Agere Systems Inc., and LSI Logic Inc. developing high-speed circuits for the telecommunication market.

Currently, his research interests include circuit verification with emphasis on the application of UVM methodologies, bioengineering with the focus in signal processing of biological signals, and the application of bio-inspired methods to the design of evolvable hardware. He also leads projects with companies of the aerospace sector to verify their chip designs.